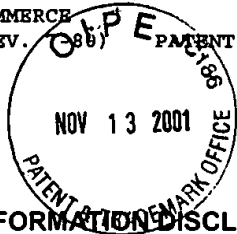
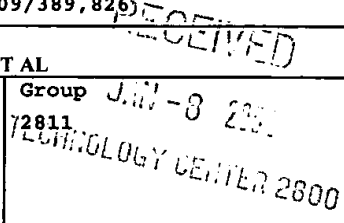


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| Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-88) PATENT AND TRADEMARK OFFICE <div style="text-align: center;">  </div> | | | | DOCKET NO. PHN 17,073 | | Serial No. 09/389,825 | |
| INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) | | | | Applicant HANS U. SCHROEDER ET AL | | <div style="text-align: center;">  </div> | |
| | | | | Filing Date September 3, 1999 | | | |

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| | | Yes | No | | | | | | | | | | | | | | | |
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